



**EE 301 / 501**

## **Circuit Analysis I**

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**775-5022 / 313 RC**  
**Office Hours / 2:00-3:00 TR**

**Spring 2005**  
**12:20- 2:00 pm / TR**  
**144 RC**

**Description:** The course is concerned with the development of basic modeling and analysis techniques and understanding the operational characteristics of electrical circuits.

**Chapter:**

- 1 Introduction
- 2 Circuit elements
- 3 Resistive circuits
- 4 Techniques of circuit analysis
- 5.1 to 5.6 The operational amplifier
- 6 Inductance and capacitance
- 7.1 to 7.5 Response of first-order RL and RC circuits
- 8.1 to 8.4 RLC circuits
- 9.1 to 9.7 Sinusoidal steady-state analysis

**Text:** *Electric Circuits*, Kang/James W. Nilsson and Susan A. Riedel, Prentice Hall, 2005.

**Lab:** EE301 is to be taken concurrently with or before EE 302. Accordingly, if the student drops EE301, the student should drop EE302, if enrolled.

**Exams:** Exams are closed-book and closed-notes. Make-up exams may be given in cases of documented emergencies with consent of instructor. A portion of make-up exams may be oral.

**Homework:** Homework assignments will be announced in class. All problems assigned during the week will be collected every Tuesday. They will be graded and the solution will be posted in the Electrical Engineering Department.

**Notes:**

- 1) You are responsible for all assignments and announcements, which occur in class.
- 2) If necessary make-up classes will be given on Fridays during the official class time.
- 3) The instructor reserves the right to make changes.

**Grading:**

Tentative dates:	
<b>Test 1</b> , on Thursday 5/5	100 pts
<b>Test 2</b> , on Thursday 6/2	150 pts
<b>Homework</b>	<u>100 pts</u>
<b>TOTAL</b>	350 pts